

NATIONAL INSTITUTE OF TECHNOLOGY
(Formerly REGIONAL ENGINEERING COLLEGE)
TIRUCHIRAPPALLI - 620 015 TAMILNADU INDIA



TRANSCRIPT

NAME : KIRAN KUMAR KORPOL

ROLL NO. : ECB0309

DEGREE : M.Tech. (FULL TIME)

DEPARTMENT : ELECTRONICS AND COMMUNICATION ENGINEERING

SPECIALISATION : VERY LARGE SCALE INTEGRATED SYSTEM

CODE	SUBJECT	CREDIT	GRADE	PASSED
I Semester				
MVY101	GRAPH THEORY AND DISCRETE OPTIMIZATION	3	D	Dec-2003
MVY102	ANALOG VLSI	4	B	Dec-2003
MVY103	BASICS OF VLSI	3	B	Dec-2003
MVY104	DIGITAL SYSTEM DESIGN WITH PROGRAMMABLE LOGIC	3	C	Dec-2003
MVY105	VHDL ANALYSIS AND DESIGN OF DIGITAL SYSTEMS	3	C	Dec-2003
MVY106	DIGITAL SIGNAL PROCESSING STRUCTURES FOR VLSI	4	E	Dec-2003
MVY108	HDL PROGRAMMING LABORATORY	1	D	Dec-2003
II Semester				
MVY201	VLSI SYSTEM TESTING AND TESTABILITY	3	C	May-2004
MVY202	ELECTRONIC DESIGN AUTOMATION TOOLS	3	B	May-2004
MVY203	DESIGNING WITH ASICS	3	D	May-2004
MVY204	LOW POWER VLSI CIRCUITS	3	C	May-2004
MVY206	DSP ARCHITECTURE AND APPLICATIONS	3	D	May-2004
MVY208	VLSI SIMULATION LABORATORY	1	D	May-2004
MVY209	DIGITAL SIGNAL PROCESSOR PROGRAMMING LABORATORY	1	E	May-2004
III Semester				
MVY301	FPGA BASED SYSTEM DESIGN	3	E	Dec-2004
MCY301	HIGH SPEED COMMUNICATION NETWORKS	3	D	Dec-2004
MCY208	ERROR CONTROL CODING	3	C	Dec-2004
MVY308	ASIC CAD LABORATORY	1	C	Dec-2004
MVY309	PROJECT WORK - PHASE I	6	A	Dec-2004
IV Semester				
MVY408	PROJECT WORK - PHASE II	12	A	Jun-2005

SEMESTER	1	2	3	4	----	C.G.P.A. : 7.24
G.P.A.	6.76	6.65	7.19	9.00	----	CLASS : First Class

Grade Point (Relative Grading) : S-10; A-9; B-8; C-7; D-6; E-5; F-0 (Fail)

C.G.P.A. 8.5 and above (passed in first attempt)

- First class with distinction

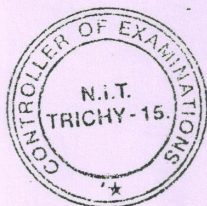
C.G.P.A. 6.5 and above but below 8.5

- First class

C.G.P.A. below 6.5

- Second class

Date: 20/07/2005



Controller of Examinations