

ANDHRA UNIVERSITY

Register No. 660752065

B. Tech. Degree Provisional Certificate cum Consolidated Memorandum of Marks

has qualified This is to certify that TRIPURA SUNITA ACHALLA Daughter of SUBRAMANYAM A himself / herself for the Degree of Bachelor of Technology Computer Science Engineering in this University, helshe having been declared to have passed the Examination prescribed therefor and that he/she has done all that is necessary for the JULY 2009 in SECOND CLASS formal presentation of the Degree.

First Year Subjects	Vear of	Marks Awarded	Pass Marks	Max. Marks
FIRST YEAR EXEMPTED ************************************				
Total				

First Semester Subjects	Year of Passing	Awarded	Pass Marks	Max. Marks
ELECTRONICS-I	NOV06	29	28	70
ELEMENTS OF ELECTRICAL ENGINEERING	NOV06	28	28	70
DATA STRUCTURES	OCT07	30	28	70
DISCRETE MATHEMETICAL STRUCTURES-I	OCT07	31	28	70
PROBABILITY STATISTICS & Q.T.	NOV06	53	28	70
DIGITAL LOGIC DESIGN	NOV06	28	28	70
ELECTRICALS LAB	DEC08	25	25	50
DATA STRUCTURES LAB	NOV06	40	25	50
SESSIONALS	NOV06	149		280
Total		413		800
Second Semester Subjects		Marks Awarded	Pass Marks	Max. Marks
ELECTRONICS-II	APR07	34	28	70
DISCRETE MATHEMATICAL STRUCTURES-II	JUN09	45	28	70
MICROPROCESSORS-I	APR07	32 57	28	70
COMPUTER ORGANIZATION	APRO7	52	28	70
OBJECT ORIENTED PROGRAMMING	APRO7	28	28	70
SYSTEMS PROGRAMMING ENVIRONMENTAL STUDIES	APR07	47	28	70
MICROPROCESSORS LAB	APR07	42	25	50
ELECTRONICS LAB	APR07	27	25	50
SESSIONALS	APR07	184		310
Total		548		900

Second Year

Novo7	, Inird Y	cai			
COMPUTER ARCHITECTURE		Year of Passing			Max. Marks
COMPUTER GRAPHICS	MICROPROCESSORS - II	NOV07	41		70
FORMAL LANGUAGES & AUTOMATA THEORY FILE STRUCTURES NOVO7 30 28 76 OPERATING SYSTEMS OPERATING SYSTEMS NOVO7 47 28 76 OPERATING SYSTEMS LAB O.O.P. LAB SESSIONALS Total Total Total Second Semester Subjects Year of Passing Awarded Marks Passing Awarded Marks Mark COMPILER DESIGN DESIGN & ANALYSIS OF ALGORITHMS DATABASE MANAGEMENT SYSTEMS DATA COMMUNICATIONS DATA COMMUNICATIONS DATA COMMUNICATIONS PRINCIPLES OF PROGRAMMING LANGUAGES APROB 38 28 7 MICROPROCESSOR - ILAB APROB 37 28 7 MICROPROCESSOR - ILAB APROB 35 28 7 MICROPROCESSOR - ILAB APROB 35 28 7 MICROPROCESSOR - ILAB APROB 37 28 7 MICROPROCESSOR - ILAB APROB 37 28 7 MICROPROCESSOR - ILAB APROB 35 25 55	COMPUTER ARCHITECTURE	NOVOB	28	28	70
NOVO7 30 28 76		NOV07	45	28	70
NOVO7	FORMAL LANGUAGES & AUTOMATA THEORY	JUL09	31		70
NOVO7 35 25 50	FILE STRUCTURES	NOV07	30	28	70
NOVO 7 32 25 50	OPERATING SYSTEMS	NOV07	47	28	70
Total 447 800		NOV07			50
Total 447 800		NOV07	32	25	50
Second Semester Year of Passing Marks Pass Marks M	SESSIONALS	NOV07	158		280
Passing Awarded Marks Marks	Total		447		800
DESIGN & ANALYSIS OF ALGORITHMS					Max. Marks
DESIGN & ANALYSIS OF ALGORITHMS					-
DATABASE MANAGEMENT SYSTEMS APRO8 50 28 7 DATA COMMUNICATIONS APRO8 38 28 7 PRINCIPLES OF PROGRAMMING LANGUAGES APRO8 40 28 7 OPERATIONS RESEARCH APRO8 37 28 7 MICROPROCESSOR - II LAB APRO8 25 25 5					1000
DATA COMMUNICATIONS APR08 38 28 7 PRINCIPLES OF PROGRAMMING LANGUAGES APR08 40 28 7 OPERATIONS RESEARCH APR08 37 28 7 MICROPROCESSOR - II LAB APR08 25 25 5	DESIGN & ANALYSIS OF ALGORITHMS	APROS	39	28	10
PRINCIPLES OF PROGRAMMING LANGUAGES APROB 40 28 7 OPERATIONS RESEARCH APROB 37 28 7 MICROPROCESSOR - II LAB APROB 25 25 5				- 00	70
OPERATIONS RESEARCH APROB 37 28 7 MICROPROCESSOR · II LAB APROB 25 25 5	DATABASE MANAGEMENT SYSTEMS				1
MICROPROCESSOR - II LAB APRO8 25 25 5	DATABASE MANAGEMENT SYSTEMS DATA COMMUNICATIONS	APR08	38	28	70
	DATABASE MANAGEMENT SYSTEMS DATA COMMUNICATIONS PRINCIPLES OF PROGRAMMING LANGUAGES	APRO8	38 40	28 28	70
	DATABASE MANAGEMENT SYSTEMS DATA COMMUNICATIONS PRINCIPLES OF PROGRAMMING LANGUAGES OPERATIONS RESEARCH	APROS APROS	38 40 37	28 28 28	70 70 70
	DATABASE MANAGEMENT SYSTEMS DATA COMMUNICATIONS PRINCIPLES OF PROGRAMMING LANGUAGES OPERATIONS RESEARCH MICROPROCESSOR - II LAB	APRO8 APRO8 APRO8 APRO8	38 40 37 25	28 28 28 25	70 70 70 70 50 50

Subjects	Year of Passing	Marks Awarded	Marks	Max. Marks
COMPUTER NETWORKS	остов	36	28	70
OBJECT ORIENTED SOFTWARE ENGINEERING	остов	28	28	70
ARTIFICIAL INTELLIGENCE	остов	34	28	70
MANAGEMENT PRINCIPLES	OCT08	38	28	70
EMBEDDED SYSTEMS	JUL09	33	28	70
GRAPHICS AND MULTIMEDIA LAB	остов	36	25	50
OBJECT ORIENTED SOFTWARE ENGINEERING LAB	остов	35	25	50
SESSIONALS	остов	180		250
Total		420		700
Second Semester Subjects	Year of Passing	Marks Awarded	Pass Marks	Max. Marks
DISTRIBUTED SYSTEMS	APR09	55	28	70
CRYPTOGRAPHY AND NETWORK SECURITY	APR09	41	28	70
DATA WARE HOUSING & DATA MINING	APR09	45	28	70
DATA COMMUNICATIONS&NETWORK PROG.LAB	JUL09	30	25	50
PROJECT		42	25	50
PROJECT	APR09	42	20	-

Total

2699 / 4500

Fourth Year

VISAKHAPATNAM

Initial of the

Read : Examined : Cal

GRAND TOTAL

800



FACULTY OF ENGINEERING

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This is to certify that

Mr./Ms._

TRIPURA SUNITA ACHALLA

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Son/Daughter of

SUBRAMANYAM A

కుమారుడు/కుమారె

has been duly admitted to the Degree of Bachelor of Technology బ్యాచులర్ ఆఫ్ టెక్నాలజీ పట్టాషధానానికి అర్హతపొందినందున

in this University he/she having been declared to have passed the examination prescribed therefor as follows : ಎಕ್ಸವಿದ್ಯಾಲಯಂ ಮಂದಿ ತಿಂದ ಮಾದಿಂದಿನ ವಿಧಂಗ್ ಅತರ್ಜ/ಆಮ ಪಟ್ಟರು ನಿರ್ದೇಶಿಂದಿನ ಪರಿಕ್ಷಲ್ ఉತ್ತಿರ್ವತ ಪಾಂದಿನಟ್ಲು ಚರಕಟಿಂದಬಹಿಂದಿ :

Subjects అభ్యసించిన అంశాలు	Class Awarded పాಂదిన (ತೆಣಿ	Medium మాధ్యమం
Computer Science Engineering	SECOND CLASS	English
	అభ్యసించిన అంశాలు	ອະທຸລິດປິດ ອະຈະເນ ລ້າດປິດ (สีก็ Computer Science Engineering SECOND

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Given under the Seal of the University ವಿಕ್ಯವಿದ್ಯಾಲಯ ಅಧಿತಾರ ಮುದ್ರತ್ ಜಾರಿವೆಯಐడಿನದಿ.

Visakhapatnam. A.P., India విశాఖపట్టణము, ఆంధ్రప్రదేశ్, భారతదేశము.

Date

04-01-2020

Controller of Examinations పరీశ్రల నిర్వహణాధికారి