



SRI VENKATESWARA UNIVERSITY

Provisional Certificate cum Consolidated Marks Memorandum

Reg. No.: 303635

This is to certify that Sri / Smt. Duggireddy Movnika has qualified himself / herself for the COMPUTER SCIENCE AND ENGINEERING Degree of Bachelor of Technology in the First Class in this University, he / she having been declared to have passed the August 2007 Examination prescribed therefor in the held in and that he / she has done all that is necessary for the formal presentation of the Degree of Bachelor of Technology.

The following are the marks secured by the candidate :

SUBJECTS	Sessional Marks		University Exam. Marks			SUBJECTS	Sessional Marks		University Exam. Marks		
	Max. Marks	Marks Secured	Max. Marks	Pass Marks	Marks Secured		Max. Marks	Marks Secured	Max. Marks	Pass Marks	Marks Secured
ENGLISH	50	34	100	35	54	ENGINEERING CHEMISTRY	50	29	100	35	42
ECONOMICS AND ACCOUNTANCY	50	42	100	35	52	COMPUTER PROGRAMMING	50	32	100	35	61
MATHEMATICS-I	50	37	100	35	64	ENGINEERING GRAPHICS	50	49	100	35	90
MATHEMATICS-II	50	24	100	35	48	PROGRAMMING LAB	50	45	50	18	42
ENGINEERING PHYSICS	50	31	100	35	40	WORKSHOP PRACTICE	50	40	50	18	40
I SEMESTER OF II YEAR						II SEMESTER OF II YEAR					
PROBABILITY AND STATISTICS	50	44	100	35	71	ELEMENTS OF ELECTRICAL ENGINEERING	50	42	100	35	64
COMPUTER ORIENTED QUANTITATIVE TECHNIQUES	50	39	100	35	51	ELEMENTS OF CIVIL AND MECHANICAL ENGINEERING	50	50	100	35	43
ELEMENTS OF ELECTRONICS AND TELECOMMUNICATIONS	50	43	100	35	44	FILE STRUCTURES	50	26	100	35	61
DISCRETE MATHEMATICAL STRUCTURES	50	45	100	35	73	COMPUTER ORGANIZATION SIMULATION AND MODELING	50	29	100	35	43
DATA STRUCTURES	50	43	100	35	60	ELECTRICAL ENGINEERING LABORATORY	50	45	50	18	35
DIGITAL LOGIC DESIGN	50	34	100	35	54	FILE STRUCTURES LABORATORY	50	41	50	18	45
ELECTRONICS AND TELECOMMUNICATIONS LABORATORY	50	45	50	18	37	ASSEMBLY LANGUAGE PROGRAMMING AND SIMULATION LABORATORY	50	37	50	18	40
DATA STRUCTURES LABORATORY	50	38	50	18	30	II SEMESTER OF III YEAR					
I SEMESTER OF III YEAR						TECHNICAL COMMUNICATION AND COMPUTER ETHICS	50	37	100	35	64
FORMAL LANGUAGES AND AUTOMATA THEORY	50	29	100	35	50	DESIGN AND ANALYSIS OF ALGORITHM	50	40	100	35	52
OPERATING SYSTEMS	50	39	100	35	48	PRINCIPLES OF PROG. LANGUAGE	50	41	100	35	35
MICROPROCESSORS	50	23	100	35	61	SYSTEMS PROGRAMMING	50	37	100	35	53
DATABASE MANAGEMENT SYSTEMS	50	39	100	35	67	DATA COMM. & COMPUTER NETWORKS	50	36	100	35	43
SOFTWARE ENGINEERING	50	34	100	35	62	OBJECT ORIENTED SOFTWARE ENGS.	50	27	100	35	63
OPERATING SYSTEMS LAB	50	39	50	18	36	SYSTEMS PROGRAMMING LABORATORY	50	39	50	18	40
MICROPROCESSORS LAB.	50	47	50	18	45	COMPUTER NETWORKS LABORATORY	50	41	50	18	42
DATABASES LAB	50	41	50	18	45	II SEMESTER OF IV YEAR					
I SEMESTER OF IV YEAR						INDUSTRIAL MANAGEMENT	50	41	100	35	63
EMBEDDED SYSTEMS & PC HARDWARE	50	32	100	35	63	ADVANCED OPERATING SYSTEMS	50	44	100	35	51
COMPILER CONSTRUCTION	50	29	100	35	57	ADVANCED COMPUTER ARCHITECTURE	50	40	100	35	46
COMPUTER GRAPHICS	50	35	100	35	65	WEB PROGRAMMING	50	34	100	35	49
ENTERPRISE RESOURCE PLANNING	50	36	100	35	67	SOFT COMPUTING	50	41	100	35	37
ARTIFICIAL INTELLIGENCE	50	39	100	35	50	SOFTWARE LABORATORY	50	40	50	18	44
EMBEDDED SYS. & PC HARDWARE LAB	50	43	50	18	31	PROJECT WORK	50	45	100	35	63
COMPILER CONSTRUCTION LAB.	50	43	50	18	35						
COMPUTER GRAPHICS LABORATORY	50	41	50	18	41						
PROJECT WORK PART-I	50	44									

12-10-2007 I to IV : 5218 (five two one eight) ✓
 II to IV : 4322 (four three two two) ✓

Date :
 Tirupati, (A.P.)

Sl. No.: TS 002934

Read by: ✓

Compared by: ✓

Suptd.: ✓

Assistant Administrative Officer (Exams)

Sri Venkateswara University

Note : Any discrepancy in the above entries or between the results published and the marks noted above, must be brought to the notice of the Controller of Examinations immediately.