



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY

HYDERABAD - 500 072.  
ANDHRA PRADESH, INDIA

## CONSOLIDATED MARKS MEMO / CREDIT SHEET

CMM. No : **A 098631**

Bachelor of Technology in **ELECTRONICS & COMMUNICATION ENGINEERING**

Serial No. : **693431707278**  
Name : **V VARDHANIMUDDABHAKTUNI**  
Hall Ticket No. : **02F01A0480**

Name of the College: **S A C E T. VETAPALEM**  
Month & Year of Final Exam: **June, 2006**  
Class Awarded: **www SECOND CLASS www**

Year of Admission **2002-2003**

S.No.	COURSE TITLE	INTERNAL MARKS	EXTERNAL MARKS	TOTAL	CREDITS	S.No.	COURSE TITLE	INTERNAL MARKS	EXTERNAL MARKS	TOTAL	CREDITS
	THEORY LAB	20 25	80 50	100 75			THEORY LAB	20 25	80 50	100 75	

### I YEAR

1	ENGLISH	15	41	56	6	2	MATHEMATICS - I	17	61	78	6
3	SOLID STATE PHYSICS	12	28	40	4	4	INFORMATION TECH. & NUME. METHODS	16	35	51	6
5	C & DATA STRUCTURES	16	30	46	6	6	NETWORK THEORY	17	28	45	6
7	ELECTRONICS DEVICES & CIRCUITS	12	28	40	6	8	ENGINEERING DRAWING PRACTICE	23	47	70	4
9	COMPUTER PROGRAMMING (LAB)	23	33	56	8	10	ELECTRONICS DEVICES & CKTS (LAB)	24	32	56	4

### I SEMESTER

### II YEAR

### II SEMESTER

1	MATHEMATICS - II	17	28	45	4	1	MATHEMATICS - III	16	28	44	4
2	ELECTRONIC CIRCUIT ANALYSIS	15	41	56	4	2	MANAGERIAL ECONOMICS & FINANCIAL A	16	29	45	4
3	PROBABILITY THEORY & STOCHASTIC PRO	16	32	48	4	3	ELECTRICAL TECHNOLOGY	16	41	57	4
4	SIGNALS & SYSTEMS	14	28	42	4	4	CONTROL SYSTEMS	14	32	46	4
5	SWITCHING THEORY & LOGIC DESIGN	19	33	52	4	5	EM WAVES & TRANSMISSION LINES	11	33	44	4
6	PULSE & DIGITAL CIRCUITS	15	28	43	4	6	COMMUNICATION THEORY	13	32	45	4
7	ELECTRONIC CIRCUITS (LAB)	23	44	67	2	7	NETWORKS & ELECTRICAL ENGG. (LAB)	22	38	60	2
8	PULSE & DIGITAL CIRCUITS (LAB)	22	35	57	2	8	ANALOG COMMUNICATIONS (LAB)	19	28	47	2

### I SEMESTER

### III YEAR

### II SEMESTER

1	MANAGEMENT SCIENCE	15	34	49	4	1	COMMUNICATION SYSTEMS	16	43	59	4
2	COMPUTER ORGANIZATION	20	43	63	4	2	ELECTRONIC MEAS. & INSTRUMENTATION	16	28	44	4
3	DIGITAL IC APPLICATIONS	15	28	43	4	3	DIGITAL SIGNAL PROCESSING	14	35	49	4
4	ANTENNAS & WAVE PROPAGATION	14	28	42	4	4	MICROWAVE ENGINEERING	15	39	54	4
5	LINEAR IC APPLICATIONS	15	52	67	4	5	V L S I DESIGN	13	45	58	4
6	DIGITAL COMMUNICATIONS	16	35	51	4	6	MICROPROCESSORS AND INTERFACING	13	28	41	4
7	LINEAR IC APPLICATIONS (LAB)	24	40	64	2	7	MICROPROCESSORS (LAB)	19	31	50	2
8	DIGITAL COMMUNICATIONS (LAB)	24	34	58	2	8	ELECTRONIC CAD (LAB)	22	35	57	2

### I SEMESTER

### IV YEAR

### II SEMESTER

1	OPERATING SYSTEMS	19	30	49	4	1	CELLULAR & MOBILE COMN.	18	34	52	4
2	COMPUTER NETWORKS	18	32	50	4	2	RADAR ENGINEERING	19	30	49	4
3	TV ENGINEERING	16	29	45	4	3	PROJECT & SEMINAR #	37	149	186	8
4	OPTICAL COMMUNICATIONS	17	30	47	4						
5	SATELLITE COMMUNICATIONS	13	28	41	4						
6	DIGITAL IMAGE PROCESSING	16	28	44	4						
7	DIGITAL SIGNAL PROCESSING LAB	21	42	63	2						
8	MICROWAVE & OPTICAL COMM. LAB	23	39	62	2						

Number of Credits registered for : **212** (# Project Internal = 40, External = 160)

Aggregate Marks Secured for best **212** credits : **2873** OUT OF 5075 ( 56.61 % )

Date of Declaration of Result **June 25, 2006**

*[Signature]*  
**CONTROLLER OF EXAMINATIONS**

(See overleaf for instruments) (\*Courses registered for but not counted for calculation of aggregate)